

AI Is Rewriting Chip Design

From vaccinators to processors, AI is now building the hardware it runs on.

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THE DATA

40%

Vaccine Efficacy Increase

MIT’s AI-driven fish vaccination system boosted delivery precision and reduced mortality.

This 40% improvement in vaccine efficacy isn't just about aquaculture—it's a proxy for how AI is optimizing physical systems at scale. When machine learning models predict optimal timing and dosage in real time, they’re not just improving outcomes; they’re demonstrating a new paradigm: AI as a co-designer of infrastructure, not just a user.

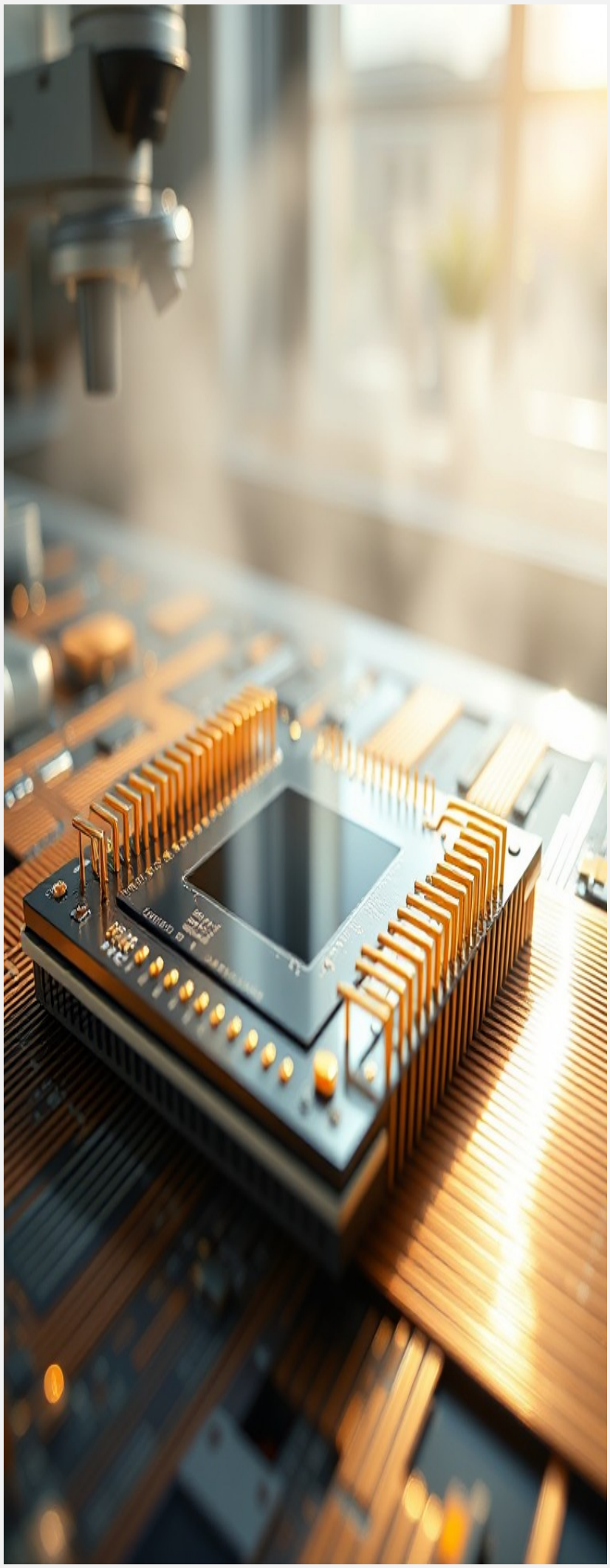
Source: wired.com
https://www.wired.com/story/new-melanoma-vaccine-reduces-risk-of-tumor-reoccurrence-by-over-40-percent/



01

AI Designs the AI Chip

The same AI that predicts the best moment to vaccinate a fish is now being used to optimize transistor layouts, power distribution, and thermal management in next-gen CPUs. This closed-loop system—where AI models trained on real-world performance data directly inform silicon architecture—is no longer speculative.



02

Hardware Is the New AI Frontier

As AI models grow larger and more complex, the bottleneck isn’t software—it’s silicon. The new race isn’t just for faster chips, but for chips that can be designed faster and better. The M6 MacBook Pro’s 1.8nm chip, with its dedicated AI accelerators and on-device neural engine, represents a shift from AI-on-chip to AI-in-chip: where the design process itself is AI-native. This means the future of semiconductors isn’t just about scaling—it’s about self-optimizing, self-aware hardware that learns from its own performance.

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